

Format for Curriculum Vitae

1. Personal Information

- **Full Name:** Vijay Kumar Lamba
- **Designation:** Associate Professor
- **Department:** Engineering & Technology/Electronics Engineering
- **Date of Birth:** 17-11-1972
- **Gender:** Male
- **Nationality:** Indian
- **Address:** Department of Engineering, Gurugram University, Gurugram
- **Mobile Number:** 9814791164
- **Email ID:** vijaykumarlamba@gurugramuniversity.ac.in (Official)
lamba_vj@hotmail.com (personal)
- **Research Profile:** <https://orcid.org/0000-0003-2893-9514>
<https://scholar.google.com/citations?user=E-ziFvoAAAAJ&hl=en>
<https://vidwan.inflibnet.ac.in/profile/277788>

- 2. Career Objective:** To contribute to excellence in teaching, research, and academic leadership in Nanoscience, Molecular Electronics, and Flexible Electronics while mentoring students, promoting innovation, and supporting institutional growth through quality education and interdisciplinary research.

- 3. Educational Qualifications**
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|--------|------------------------------|------|
| PhD | Guru Nanak Dev University | 2009 |
| M.Tech | Guru Nanak Dev University | 2002 |
| M.Sc | Panjab University | 2000 |
| BSc | Maharshi Dayanand University | 1996 |

- 4. Teaching Experience**
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|---|----------------|
| Gurugram university Gurugram | 2024 - Present |
| Symbiosis Institute of Technology (SIT) | 2022 - 2024 |
| Global College of Engg and Tech. , Anandpur Sahib | 2012 - 2022 |
| HCTM College Kaithal | 2006 - 2012 |
| BRCM College of Engineering and Technology, Bahal | 2005 - 2006 |
| Guru Jambheshwar University of Science & Technology | 2003 - 2005 |
| Total Teaching Experience: | 23 Years |

5. Research Experience

- **Areas of Specialization:** Nanoscience and Nanotechnology, Molecular Electronics, Flexible Electronics, Organic Electronics, Molecular Physics, Nanoelectronics, Charge Transport Modelling and Simulation, and Nanomaterial Design.
- **Research Projects Handled:** 04
- **Research Guidance (Ph.D.):** 11

6. Publications

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| (A) Research Papers in Journals : | 65 |
| (B) Books / Book Chapters: | 04 |
| (C) Conference Papers: | 85 |

7. Research Projects / Grants

S.N	Title	Agency	Period	Amount Mobilized
1	Modelling & Simulation of Charge Transport in Nano Devices	CSIR	2 Years	17.78 Lakhs
2	Electret based Micro-power Generators for Energy Harvesting Devices	INUP scheme	1 Year	NA
3	Development of Nano Science & Technology Laboratory	AICTE	1 Year	10.00 Lakhs

8. Conferences / FDPs / Workshops Attended: 85

9. Awards & Achievements:

- Visiting Fellow, JNC SAR, Bangalore, No: JNC/F&E/VF-0102(CPMU-4) 2010-2011-505 (TSU, JNC SAR, Bangalore)(2010-2011)
- INFROM (European project) Grant for Nano Formulation, for visiting Sweden and presenting research work at Nano Formulation -2010
- DST Fellowship- SR/ITS/2009/01596 for visiting USA (2009)
- Nano-DDS Fellowship, for presenting research work at Fort Lauderdale, USA (2009)
- AICTE Fellowship (Under TG Scheme, no 1-7RID/TR(192) for visiting USA (2008)
- CSIR-UGC Fellowship 2001

10. Skills Summary

- Modelling & Simulation using NEGF approach, DFT
- Epitaxial growth of GaAs nanostructures (nanodots and nanopillars), heterostructures, ferromagnetic materials, using Molecular Beam Epitaxy (MBE).
- Chemical Vapor Deposition (CVD) growth of nanostructures/ microstructures of 2-dimensional transition metal dichalcogenide (MoS_2 , WS_2) on Si and sapphire substrates.
- Thin film deposition using e-beam evaporator, Plasma Enhanced Chemical Vapour Deposition (PECVD).
- Characterization: Scanning Electron Microscopy (SEM), Atomic Force Microscopy (AFM), Transmission Electron Microscopy (TEM), and Energy Dispersive X-Ray Spectroscopy (EDX).
- Fabrication of Alumina (Al_2O_3) Templates using two-step anodization procedure.
- Nanopatterning of Si and GaAs substrate using alumina templates.
- Working knowledge of vacuum systems.
- Working knowledge of hazardous materials handling and emergency response procedures.
- Significant experience of working in a clean-room environment